Library description • 03/2016

Library for Controlled System Simulation with STEP 7 (TIA Portal)

STEP 7 V13 SP1

https://support.industry.siemens.com/cs/ww/en/view/79047707

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Table of contents

1 Library Overview

What you get

This document describes the “LSim” block library. The block library provides you with the tested code with clearly defined interfaces. They can be used as a basis for your task to be implemented.

A key concern of the document is to describe

- all blocks of the block library
- the functionality implemented through these blocks.

Furthermore, this documentation shows possible fields of application and helps you integrate the library into your STEP 7 project using step-by-step instructions.

1.1 Different user scenarios

Possible application(s) for the “LSim” library

The “LSim” library allows you to simulate controlled systems in an S7 CPU.

The blocks of the library simulate various simple controlling elements through linear transfer elements. Suitable serial or parallel interconnection of the individual system elements allows you to simulate even very complex controlled systems.

The library can be used, for example, in the following scenarios:

- Optimizing a controller: Simulation of the controlled system where commissioning on the real process is difficult or hardly possible.
- For training purposes: Simulation of individual controlled system elements for a clear representation of control engineering processes and demonstration of software controllers.

1.2 Hardware and software requirements

Requirements for this library

To be able to use the functionality of the library described in this document, the following hardware and software requirements must be met:

Hardware

The library was tested with CPUs of the SIMATIC S7-1200 and S7-1500 series.

However, other SIMATIC controllers can also be used as hardware.

Software

Table 1-1

Component	Article number
STEP 7 V13 SP1 (TIA Portal V13 SP1)	6ES7822-1AE03-0YA5

Note The SCL blocks can be very easily ported to STEP 7 V5.5.

1.3 Library resources

What will you find in this section?

The following section gives you an overview of the size of the blocks of the “LSim” library in the main memory.

Size of the individual blocks

Table 1-2

FB no.	Name	S7-1200 allocation (in bytes)		S7-1500 allocation (in bytes)	
		Load memory	**Main memory**	**Load memory**	**Main memory**
50	LSim_PT1	7438	287	7451	358
51	LSim_PT1asym	9747	443	9721	514
52	LSim_PT2osc	11651	639	11676	710
53	LSim_PT2aper	10982	606	10927	677
54	LSim_PT3	9966	563	10004	629
55	LSim_PDT1	9417	428	9381	500
56	LSim_I	7646	282	7688	361
57	LSim_IT1	9820	495	9836	566
58	LSim_TempProcess	9568	414	9528	485
59	LSim_Lagging	5556	220	5542	284
60	LSim_DT1	8591	353	8592	425
61	LSim_Allpass1OrdReal	8326	354	8323	425
62	LSim_Allpass2OrdReal	11009	615	10987	687
63	LSim_Valve	16110	496	16275	537
64	LSim_PT3HeatCool	13871	801	13835	866
	Total allocation	**149698**	**6996**	**149766**	**8024**

2 Blocks of the Library

What will you find in this section?

This chapter lists (chapter 2.1) and explains (chapter 2.2) all blocks of the "LSim" library.

2.1 List of the blocks

The following table lists all the blocks of the "LSim" library.

Chapter 2.2 provides a detailed description of the blocks.

Table 2-1

Block	Name	Brief description	
FB50	LSim_PT1	Simulation of a PT1 system	Self-regulating systems
FB51	LSim_PT1asym	Simulation of an asymmetrical PT1 system	
FB52	LSim_PT2osc	Simulation of a PT2 system in the periodic case	
FB53	LSim_PT2aper	Simulation of a PT2 system in the aperiodic case	
FB54	LSim_PT3	Simulation of a PT3 system	
FB55	LSim_PDT1	Simulation of a PDT1 system	
FB58	LSim_TempProcess	Simulation of a temperature process	
FB60	LSim_DT1	Simulation of a DT1 system	
FB61	LSim_Allpass1OrdReal	Simulation of a first-order all-pass	
FB62	LSim_Allpass2OrdReal	Simulation of a second-order all-pass	
FB64	LSim_PT3HeatCool	Simulation of a PT3 system with separate inputs for heating and cooling	
FB56	LSim_I	Simulation of an I system	Systems without inherent regulation
FB57	LSim_IT1	Simulation of an IT1 system	
FB63	LSim_Valve	Simulation of a valve	
FB59	LSim_Lagging	Simulation of a lag element	

2.2 Explanation of the blocks

This chapter introduces the parameters of the blocks.
For some parameters, it shows the transfer function of the block and the step response of the respective block.

2.2.1 FB „LSim_PT1“ (FB 50)

FB “LSim_PT1” (FB50) simulates a PT1 element. The PT1 element is a proportional transfer element with first-order order delay.

Use case

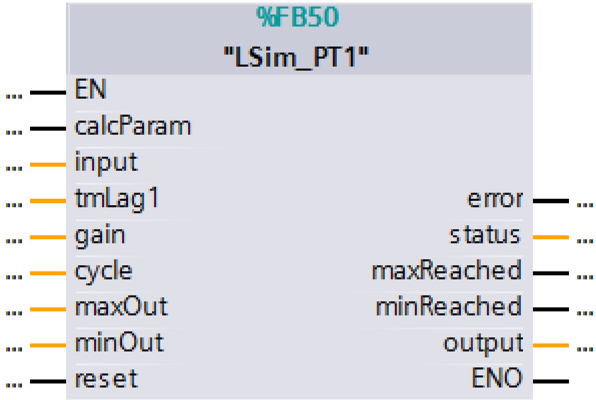
A PT1 element can be used, for example, to simulate a temperature system.

Transfer function

$$F(p) = \frac{gain}{tmLag1 * p + 1}$$

Figure

Figure 2-1: PT1 element



Input parameters

Table 2-2

Parameter	Data type	Description
calcParam	Bool	Recalculation of the internal parameters (activation required when “tmLag1”, “gain” or “cycle” changes)
input	Real	Input variable
tmLag1	Real	Time constant (in seconds)
gain	Real	Gain factor
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
maxOut	Real	Maximum limit of the output signal (when the signal is above the maximum limit, “output” = “maxOut” will be set)
minOut	Real	Minimum limit of the output signal (when the signal is below the minimum limit, “output” = “minOut” will be set)
reset	Bool	Resets all relevant parameters to ‘0’, including “output”.

Output parameters

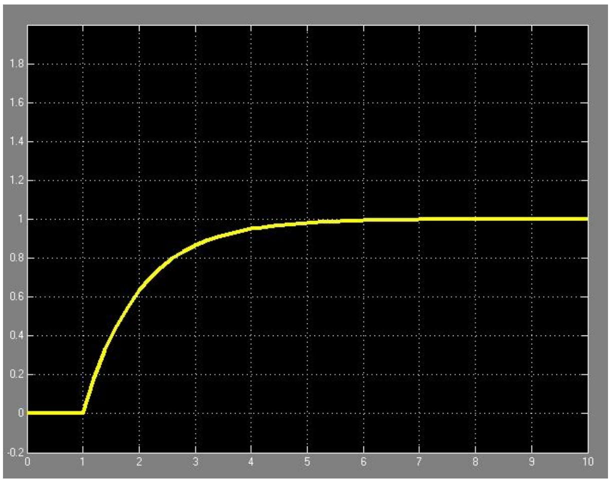
Table 2-3

Parameter	Data type	Description
error	Bool	If “tmLag1” <= 0, “error” = TRUE will be set.
status	Word	If “tmLag1” <= 0, “status” = 16#8001 will be set.
maxReached	Bool	When “maxReached” = TRUE, the “output” output variable was limited to the maximum value “maxOut”.
minReached	Bool	When “minReached” = TRUE, the “output” output variable was limited to the minimum value “minOut”.
output	Real	Output variable (valid only when “error” = FALSE)

Step response

$$y(t) = gain * (1 - e^{-\frac{tmLag1}{t}})$$

Figure 2-2: PT1 element step response



2.2.2 FB „LSim_PT1asym” (FB 51)

FB “LSim_PT1asym” (FB 51) simulates a PT1 system whose time response differs depending on whether the trigger is positive or negative. This makes it an asymmetrical PT1 system.

Use case

An asymmetrical PT1 element can be used, for example, to simulate a temperature system with different behavior for heating and cooling.

Transfer function

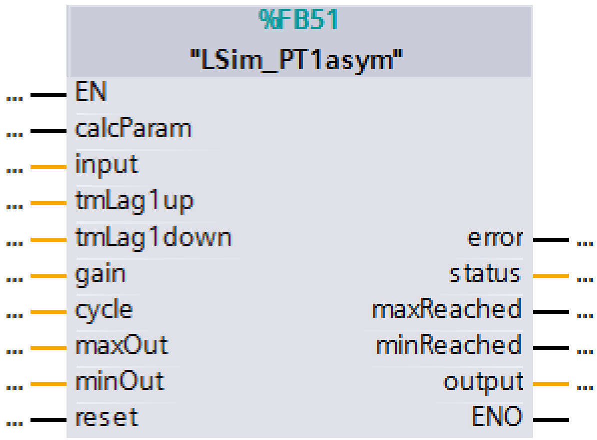
Unlike FB “LSim_PT1”, the transfer function internally consists of two different PT1 elements for the up and down movement of the process value.

$$F_{up}(p) = \frac{gain}{tmLag1up * p + 1}$$

$$F_{down}(p) = \frac{gain}{tmLag1down * p + 1}$$

Figure

Figure 2-3: Asymmetrical PT1 element



Input parameters

Table 2-4

Parameter	Data type	Description
calcParam	Bool	Recalculation of the internal parameters (activation required when “tmLag1up”, “tmLag1down”, “gain” or “cycle” changes)
input	Real	Input variable
tmLag1up	Real	Time constant (in seconds) in the event of positive triggering (“input” > “output”)
tmLag1down	Real	Time constant (in seconds) in the event of negative triggering (“input” < “output”)
gain	Real	Gain factor
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
maxOut	Real	Maximum limit of the output signal (when the signal is above the

Parameter	Data type	Description
		maximum limit, “output” = “maxOut” will be set)
minOut	Real	Minimum limit of the output signal (when the signal is below the minimum limit, “output” = “minOut” will be set)
reset	Bool	Resets all relevant parameters to ‘0’, including “output”.

Output parameters

Table 2-5

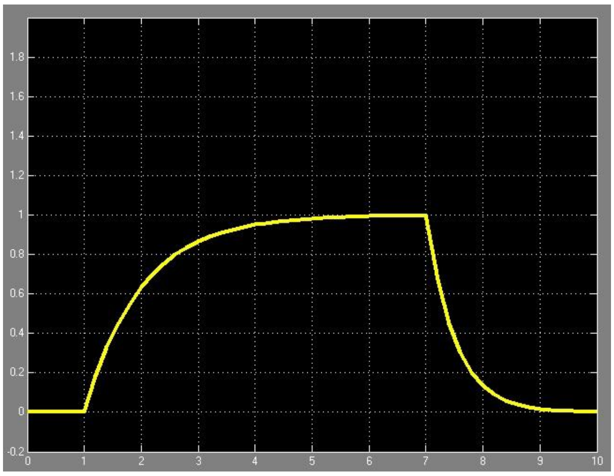
Parameter	Data type	Description
error	Bool	If “tmLag1up” <= 0 or “tmLag1down” <= 0, “error” = TRUE will be set.
status	Word	If “tmLag1up” <= 0 or “tmLag1down” <= 0, “status” = 16#8001 will be set.
maxReached	Bool	When “maxReached” = TRUE, the “output” output variable was limited to the maximum value “maxOut”.
minReached	Bool	When “minReached” = TRUE, the “output” output variable was limited to the minimum value “minOut”.
output	Real	Output variable (valid only when “error” = FALSE)

Step response

$$y(t) = gain * (1 - e^{-\frac{tmLag1up}{t}})$$ for a positive unit step

$$y(t) = gain * (1 - e^{-\frac{tmLag1down}{t}})$$ for a negative unit step

Figure 2-4: Asymmetrical PT1 element step response



2.2.3 FB „LSim_PT2osc" (FB52)

FB "Sim_PT2osc" (FB52) simulates an oscillating PT2 system. Accordingly, the damping factor selected for this system must be less than 1. Accordingly, the system is in the periodic case.

Use case

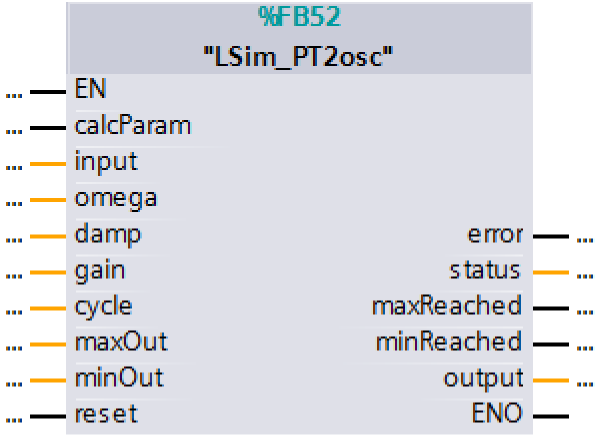
A periodic PT2 element can be used, for example, to simulate mechanical systems that execute a stroke/rotation and are oscillating.

Transfer function

$$F(p) = \frac{gain}{(\frac{p}{omega})^2 + 2 * damp * \frac{p}{omega} + 1}$$

Figure

Figure 2-5: PT2 element in the periodic case



Input parameters

Table 2-6

Parameter	Data type	Description
calcParam	Bool	Recalculation of the internal parameters (activation required when "omega", "damp", "gain" or "cycle" changes)
input	Real	Input variable
omega	Real	Angular frequency of the free oscillation
damp	Real	Damping ratio: 0 < "damp" < 1
gain	Real	Gain factor
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
maxOut	Real	Maximum limit of the output signal (when the signal is above the maximum limit, "output" = "maxOut" will be set)
minOut	Real	Minimum limit of the output signal (when the signal is below the minimum limit, "output" = "minOut" will be set)
reset	Bool	Resets all relevant parameters to '0', including "output".

Output parameters

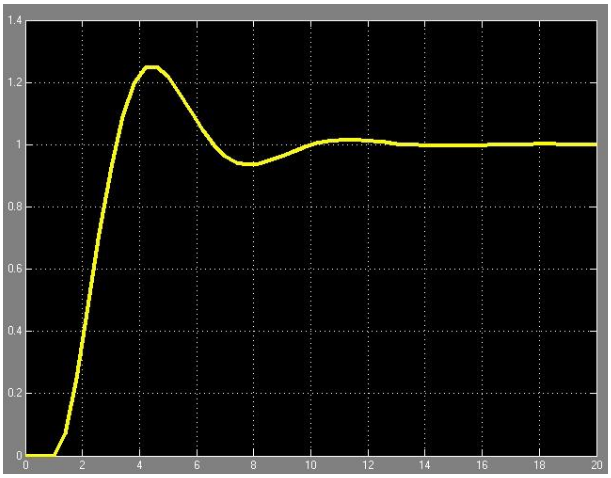
Table 2-7

Parameter	Data type	Description
error	Bool	If the conditions 0 < “damp” < 1 or “omega” > 0 are not met, “error” = TRUE will be set.
status	Word	If the conditions 0 < “damp” < 1 or “omega” > 0 are not met, “status” = 16#8001 will be output.
maxReached	Bool	When “maxReached” = TRUE, the “output” output variable was limited to the maximum value “maxOut”.
minReached	Bool	When “minReached” = TRUE, the “output” output variable was limited to the minimum value “minOut”.
output	Real	Output variable (valid only when “error” = FALSE)

Step response

$$y(t) = gain - \frac{gain}{\sqrt{1-damp^2}} e^{-damp*tmLag1*T} * \sin\left[\frac{\sqrt{1-damp^2}}{\frac{1}{tmLag1}} t + \arctan(\frac{\sqrt{1-damp^2}}{damp})\right]$$

Figure 2-6: PT2 element with 0<“damp”<1



2.2.4 FB „LSim_PT2aper" (FB 53)

FB "LSim_PT2aper" (FB53) simulates a PT2 system with a damping factor>1. The system is in the aperiodic case where no overshoot occurs.

Use case

An aperiodic PT2 element can be used, for example, to simulate a spring pendulum.

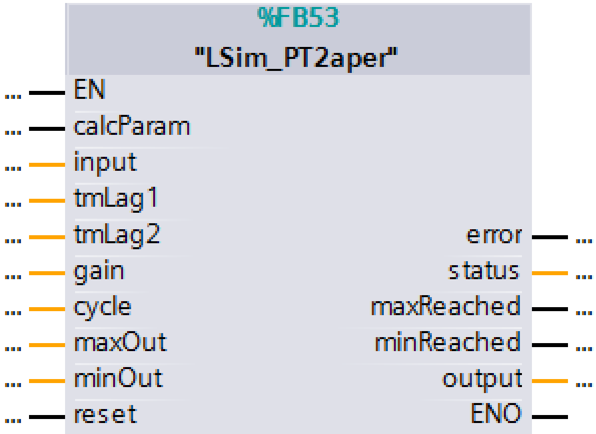
Transfer function

Unlike FB "LSim_PT2osc", the transfer function internally consists of two PT1 elements connected in series:

$$F(p) = \frac{gain}{(tmLag1 \ * p + 1) * (tmLag2 * p + 1)}$$

Figure

Figure 2-7



Input parameters

Table 2-8

Parameter	Data type	Description
calcParam	Bool	Recalculation of the internal parameters (activation required when "tmLag1up", "tmLag1down", "gain" or "cycle" changes)
input	Real	Input variable
tmLag1	Real	Time constant for the first PT1 element (in seconds)
tmLag2	Real	Time constant for the second PT1 element (in seconds)
gain	Real	Gain factor
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
maxOut	Real	Maximum limit of the output signal (when the signal is above the maximum limit, "output" = "maxOut" will be set)
minOut	Real	Minimum limit of the output signal (when the signal is below the minimum limit, "output" = "minOut" will be set)

Parameter	Data type	Description
reset	Bool	Resets all relevant parameters to ‘0’, including “output”.

Output parameters

Table 2-9

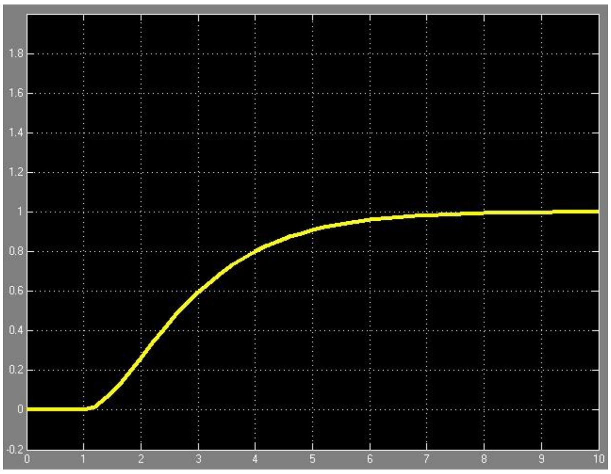
Parameter	Data type	Description
error	Bool	If the time constants are identical or <= 0, “error” = TRUE will be set.
status	Word	If the time constants are identical or <= 0, “status” = 16#8001 will be output.
maxReached	Bool	When “maxReached” = TRUE, the “output” output variable was limited to the maximum value “maxOut”.
minReached	Bool	When “minReached” = TRUE, the “output” output variable was limited to the minimum value “minOut”.
output	Real	Output variable (valid only when “error” = FALSE)

Step response

$$y(t) = gain - \frac{gain}{tmLag1 - tmLag2}\left[tmLag1\ e^{\frac{-t}{tmLag1}} - tmLag2\ e^{\frac{-t}{tmLag2}}\right]$$

$$tmLag1 <> tmLag2$$

Figure 2-8: Step response of PT2 element with D>1



2.2.5 FB „LSim_PT3" (FB54)

FB "LSim_PT3" (FB54) simulates a third-order delay element.
Internally, it consists of the serial interconnection of three PT1 systems.

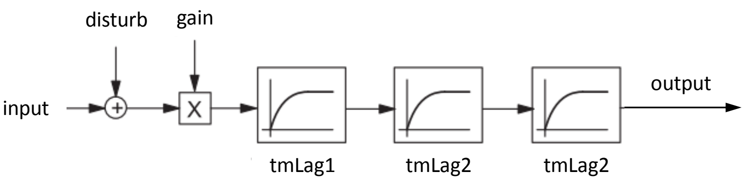
Use case

A PT3 element can be used, for example, to simulate a temperature system with multiple storage elements.

Transfer function

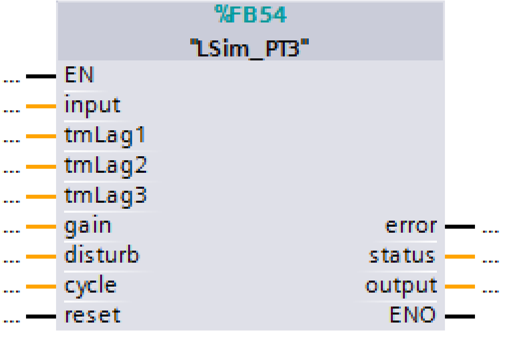
$$F(p) = \frac{gain}{(tmLag1 * p \; + 1) * (tmLag2 \; * p + 1) * (tmLag3 \; * p + 1)}$$

Figure 2-9: Structure and parameters of the "LSim_PT3" system block



Figure

Figure 2-10: PT3 element



Input parameters

Table 2-10

Parameter	Data type	Description
input	Real	Input variable
tmLag1	Real	Time constant for the first PT1 element (in seconds)
tmLag2	Real	Time constant for the second PT1 element (in seconds)
tmLag3	Real	Time constant for the third PT1 element (in seconds)
gain	Real	Gain factor
disturb	Real	Disturbance variable (applied at the "input" input)
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
reset	Bool	Resets all relevant parameters (including "output" = "input").

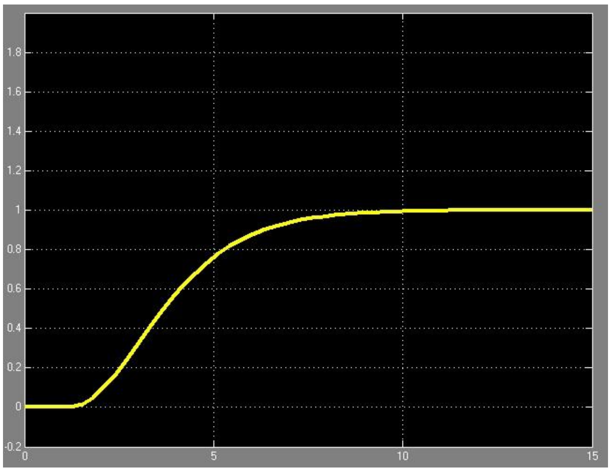
Output parameters

Table 2-11

Parameter	Data type	Description
error	Bool	If one of the time constants <= 0, “error” = TRUE will be set.
status	Word	If one of the time constants <= 0, “status” = 16#8001 will be output.
output	Real	Output variable (valid only when “error” = FALSE)

Step response

Figure 2-11: PT3 element step response



2.2.6 FB „LSim_PDT1" (FB55)

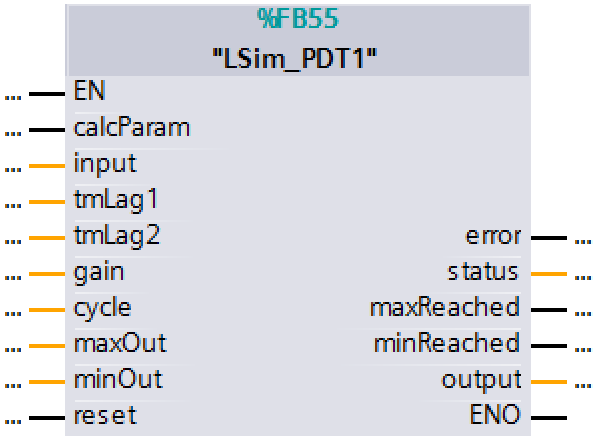
FB "LSim_PDT1" (FB55) simulates the behavior of a PDT1 element (generic rational first-order element).

Transfer function

$$F(p) = \frac{gain * (1 + tmLag2 * p)}{(1 + tmLag1 * p)}$$

Figure

Figure 2-12



Input parameters

Table 2-12

Parameter	Data type	Description
calcParam	Bool	Recalculation of the internal parameters (activation required when "tmLag1", "tmLag2", "gain" or "cycle" changes)
input	Real	Input variable
tmLag1	Real	First time constant (in seconds)
tmLag2	Real	Second time constant (in seconds)
gain	Real	Gain factor
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
maxOut	Real	Maximum limit of the output signal (when the signal is above the maximum limit, "output" = "maxOut" will be set)
minOut	Real	Minimum limit of the output signal (when the signal is below the minimum limit, "output" = "minOut" will be set)
reset	Bool	Resets all relevant parameters to '0', including "output".

Output parameters

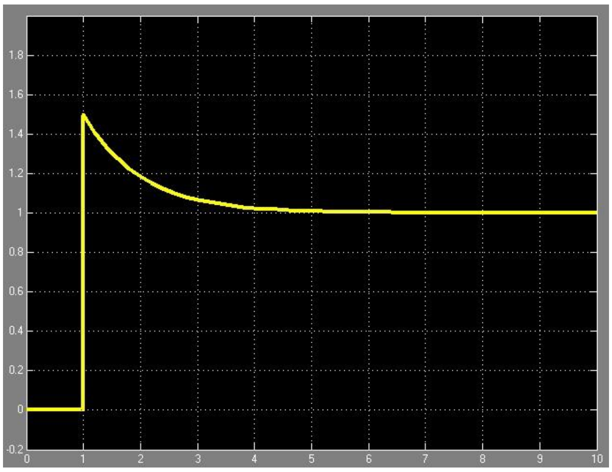
Table 2-13

Parameter	Data type	Description
error	Bool	If one of the time constants <= 0, “error” = TRUE will be set.
status	Word	If one of the time constants <= 0, “status” = 16#8001.
maxReached	Bool	When “maxReached” = TRUE, the “output” output variable was limited to the maximum value “maxOut”.
minReached	Bool	When “minReached” = TRUE, the “output” output variable was limited to the minimum value “minOut”.
output	Real	Output variable (valid only when “error” = FALSE)

Step response

$$y(t) = GAIN\left(1 + \frac{(tmLag2 - tmLag1)}{tmLag1} * e^{-\frac{t}{tmLag1}}\right);\ tmLag1 > 0$$

Figure 2-13: PDT1 element step response (*tmLag1/tmLag2 < 1*)



2.2.7 FB „LSim_I" (FB56)

FB "LSim_I" (FB56) simulates a simple integrating system.

Use case

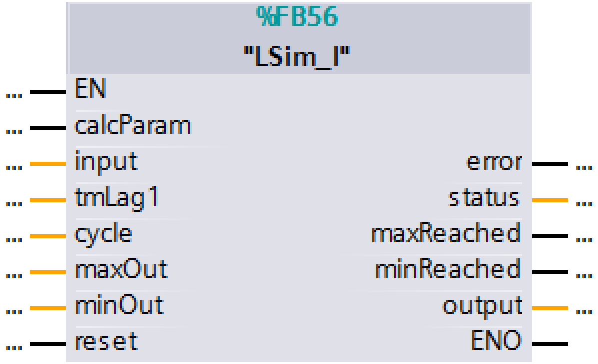
An I element can be used, for example, to simulate a fill level system (container).

Transfer function

$$F(p) = \frac{1}{(tmLag1 * p)}$$

Figure

Figure 2-14



Input parameters

Table 2-14

Parameter	Data type	Description
calcParam	Bool	Recalculation of the internal parameters (activation required when "tmLag1" or "cycle" changes)
input	Real	Input variable
tmLag1	Real	Integral action time (in seconds)
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
maxOut	Real	Maximum limit of the output signal (when the signal is above the maximum limit, "output" = "maxOut" will be set)
minOut	Real	Minimum limit of the output signal (when the signal is below the minimum limit, "output" = "minOut" will be set)
reset	Bool	Resets all relevant parameters to '0', including "output".

Output parameters

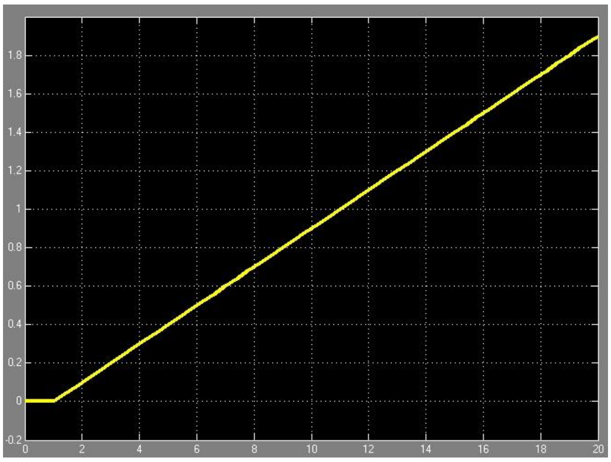
Table 2-15

Parameter	Data type	Description
error	Bool	If the integral action time <= 0, “error” = TRUE will be set.
status	Word	If the integral action time <= 0, “status” = 16#8001.
maxReached	Bool	When “maxReached” = TRUE, the “output” output variable was limited to the maximum value “maxOut”.
minReached	Bool	When “minReached” = TRUE, the “output” output variable was limited to the minimum value “minOut”.
output	Real	Output variable (valid only when “error” = FALSE)

Step response

$$y(t) = \frac{t}{tmLag1}, \qquad t \geq 0$$

Figure 2-15: I element step response



2.2.8 FB „LSim_IT1" (FB 57)

FB "LSim_IT1" (FB57) simulates a delayed integrator.

Use case

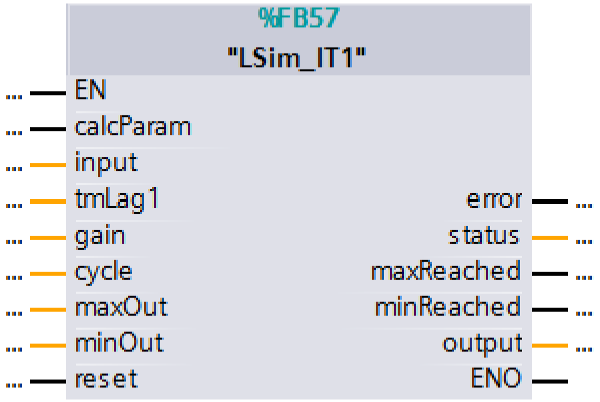
An IT1 element can be used, for example, to simulate a valve with servomotor.

Transfer function

$$F(p) = \frac{gain}{p * (tmLag1 * p + 1)}$$

Figure

Figure 2-16



Input parameters

Table 2-16

Parameter	Data type	Description
calcParam	Bool	Recalculation of the internal parameters (activation required when "tmLag1", "gain" or "cycle" changes)
input	Real	Input variable
tmLag1	Real	Integral action time (in seconds)
gain	Real	Gain factor
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
maxOut	Real	Maximum limit of the output signal (when the signal is above the maximum limit, "output" = "maxOut" will be set)
minOut	Real	Minimum limit of the output signal (when the signal is below the minimum limit, "output" = "minOut" will be set)
reset	Bool	Resets all relevant parameters to '0', including "output".

Output parameters

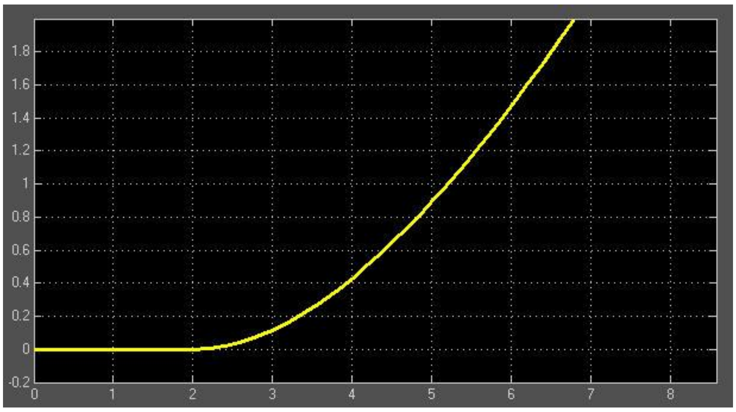
Table 2-17

Parameter	Data type	Description
error	Bool	If the integral action time <= 0, “error” = TRUE will be set.
status	Word	If the integral time <= 0, “status” = 16#8001.
maxReached	Bool	When “maxReached” = TRUE, the “output” output variable was limited to the maximum value “maxOut”.
minReached	Bool	When “minReached” = TRUE, the “output” output variable was limited to the minimum value “minOut”.
output	Real	Output variable (valid only when “error” = FALSE)

Step response

$$y(t) = gain[t - tmLag1(1 - e^{\frac{-t}{tmLag1}})]$$

Figure 2-17: IT1 element step response



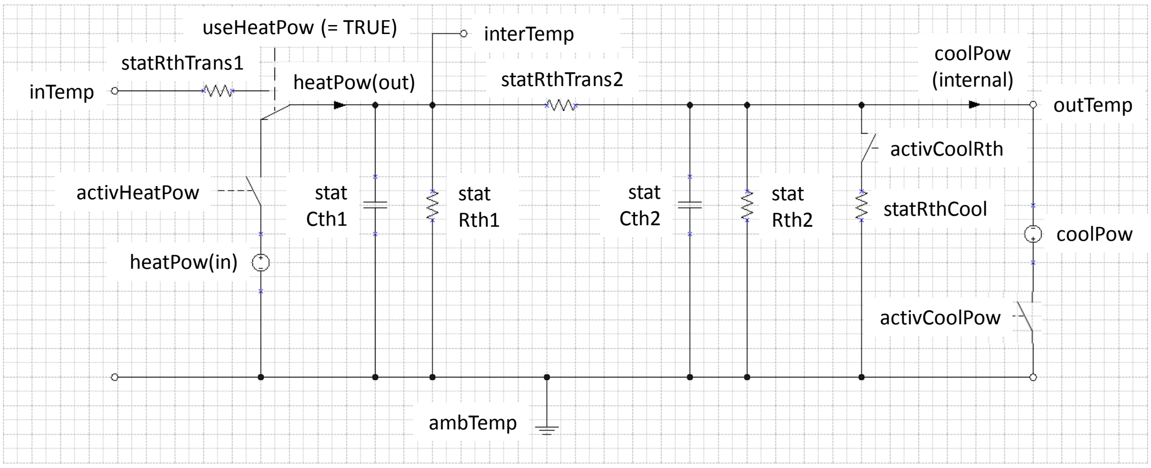
2.2.9 FB „LSim_TempProcess" (FB58)

FB "LSim_TempProcess" (FB58) simulates an asymmetrical temperature process that can be actively heated (using aids) and cooled (both actively and passively).

Connection diagram

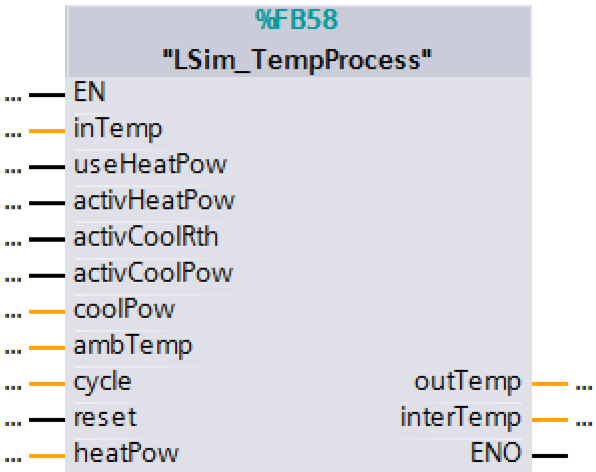
FB "LSim_TempProcess" (FB58) uses the following equivalent connection diagram:

Figure 2-18



Figure

Figure 2-19



Parameters

Table 2-18

Parameter	Data type	Description
inTemp	IN: Real	Temperature input value (usable only when “useHeatPow” = FALSE)
useHeatPow	IN: Bool	FALSE: “inTemp” is used as the heat source TRUE: “heatPow” in conjunction with “activHeat” is used as the heat source.
activHeat	IN: Bool	Activate heating with “heatPow” (usable only when “useHeatPow” = TRUE)
activCoolRth	IN: Bool	Active cooling to ambient temperature via internal thermal resistance “statRThCool”
activCoolPow	IN: Bool	Active cooling via cooling power “coolPow” (even below ambient temperature)
coolPow	IN: Real	Cooling power as a positive value in W (active only when “activCoolPow” = TRUE)
ambTemp	IN: Real	Ambient temperature
cycle	IN: Real	Cycle time of the calling cyclic interrupt OB (in seconds)
reset	IN: Bool	Reset all relevant parameters (“outTemp” = “ambTemp”).
heatPow	INOUT: Real	Heating power in W (as an input when “useHeatPow” = TRUE and “activHeat” = TRUE / as an output when “useHeatPow” = FALSE)
outTemp	OUT: Real	Output temperature
interTemp	OUT: Real	Intermediate temperature (auxiliary quantity)
statRthTrans1	Static: Real	Input thermal resistance in K/W (active only when “useHeatPow” = FALSE)
statRth1	Static: Real	Thermal resistance parallel to thermal capacity 1 in K/W
statRthTrans2	Static: Real	Thermal resistance for heat flow between thermal capacity 1 and 2 in K/W
statRth2	Static: Real	Thermal resistance parallel to thermal capacity 2 in K/W
statRthCool	Static: Real	Thermal resistance for active cooling in K/W (active only when “activCoolRth” = TRUE)
statCth1	Static: Real	Thermal capacity 1 in Ws/K
statCth2	Static: Real	Thermal capacity 2 in Ws/K

Possible applications

The following applications can be implemented with FB “LSim_TempProcess” (heating and cooling can also be combined):

Table 2-19: Selection of heating options (1 out of 3)

Heating	**via power input “heatPow”**		**via temperature input “inTemp” (for an example, see Figure 2-20)**
Parameter	**analog**	**PWM signal**	
inTemp	irrelevant	irrelevant	variable input quantity
useHeatPow	TRUE	TRUE	FALSE
activHeat	TRUE	PWM signal	irrelevant
heatPow	variable input quantity	constant input quantity	output quantity

Table 2-20: Selection of cooling options with power setting (1 out of 2)

Cooling even below ambient temperature (with cooling power setting)		
Parameter	**analog**	**PWM signal**
activCoolPow	FALSE	PWM signal
coolPow	variable input quantity	constant input quantity

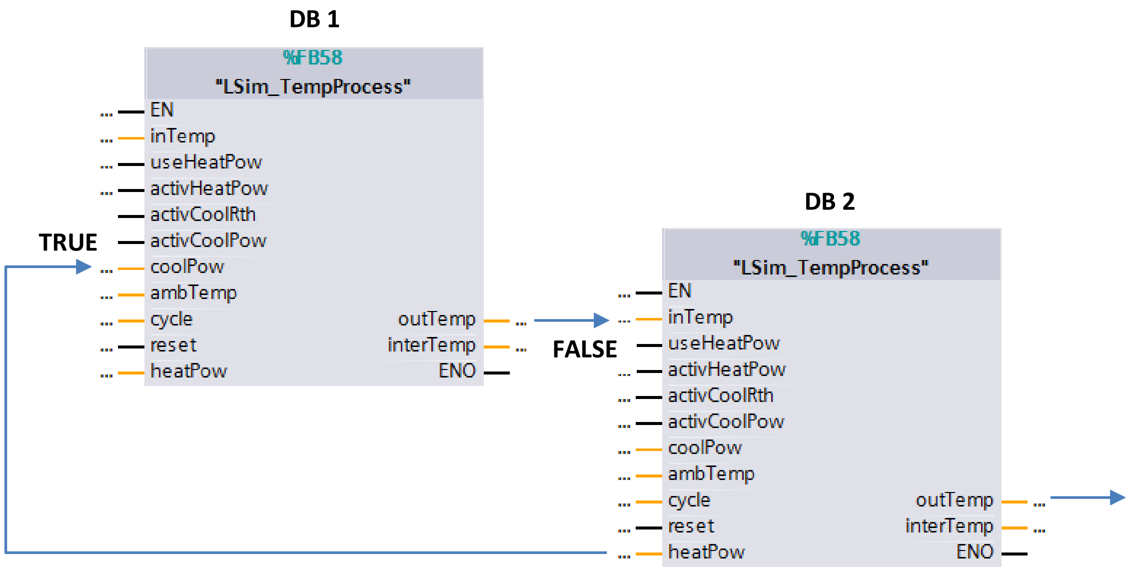
Table 2-21: Selection of cooling options via thermal resistance (coexistent)

Cooling to ambient temperature (via thermal resistance)		
Parameter	**Active**	**Passive**
Thermal resistance	statRthCool	statRth2
activCoolRth	TRUE	

Cascading

To map more complex processes, “LSim_TempProcess” can also be cascaded as follows:

Figure 2-20: Thermal influence of neighboring processes (cascade)



The first process is heated. The determined temperature “outTemp” of the first instance (DB1) is used as the input temperature “inTemp” of the second instance (DB2). This requires that input “useHeatPow” (DB2) be reset (FALSE). Therefore, the second process extracts energy from the first process. This means that the determined power “heatPow” of the second instance (DB2) must be returned to the first instance as cooling power “coolPow”. This requires that input “activCoolPow” (DB1) be set (TRUE).

Each process can be additionally cooled passively with “activCoolRth” via the thermal resistance. The second process, in turn, can be cooled actively via “coolPow” in conjunction with “activCoolPow” (for example, by the next instance).

2.2.10 FB „LSim_Lagging" (FB59)

FB "LSim_Lagging" (FB59) simulates a lag element.

Use case

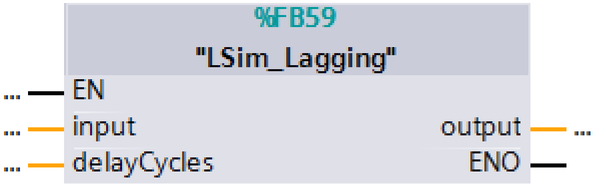
FB "Sim_Lagging" (FB59) allows you to simulate, for example, conveyor systems.

Transfer function

$$F(p) = e^{-p*delayCycles*cycle}$$

Figure

Figure 2-21



Parameters

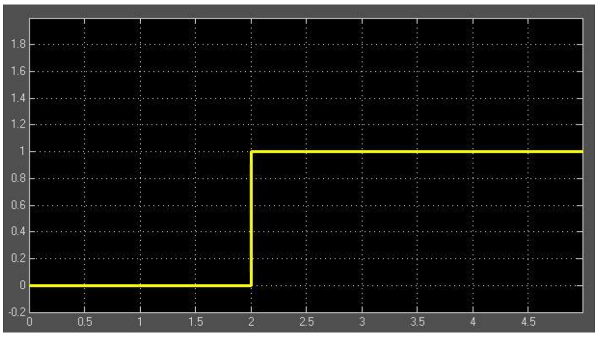
Table 2-22

Parameter	Data type	Description
input	IN: Real	Input variable
delayCycles	INOUT: UInt	Number of cycles by which the input signal is delayed (max. internal constant "MAX"; when exceeded, "delayCycles" = "MAX" will be set).
output	OUT: Real	Output variable
MAX	Constant: Int	Upper limit of the field for buffering the input signal (default: 100)

Step response

$$y(t) = u(t - delayCycles * cycle)$$

Figure 2-22: Lag element step response



2.2.11 FB „ LSim_DT1" (FB60)

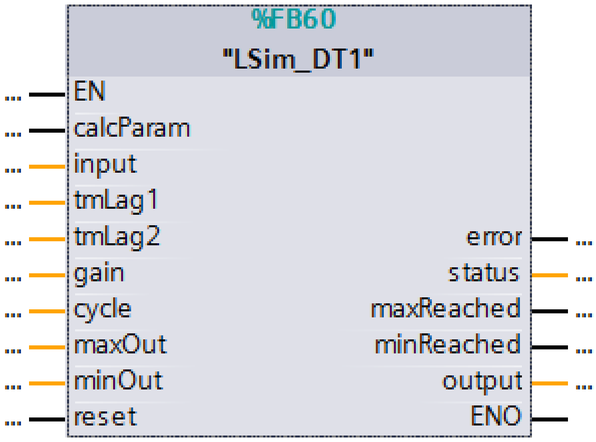
FB "LSim_DT1" (FB60) simulates a delaying derivative action element.

Transfer function

$$F(p) = \frac{gain * tmLag2 * p}{(tmLag1 * p + 1)}$$

Figure

Figure 2-23



Input parameters

Table 2-23

Parameter	Data type	Description
calcParam	Bool	Recalculation of the internal parameters (activation required when "tmLag1", "tmLag2", "gain" or "cycle" changes)
input	Real	Input variable
tmLag1	Real	Derivative action time
tmLag2	Real	Delay time
gain	Real	Gain factor
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
maxOut	Real	Maximum limit of the output signal (when the signal is above the maximum limit, "output" = "maxOut" will be set)
minOut	Real	Minimum limit of the output signal (when the signal is below the minimum limit, "output" = "minOut" will be set)
reset	Bool	Resets all relevant parameters to '0', including "output".

Output parameters

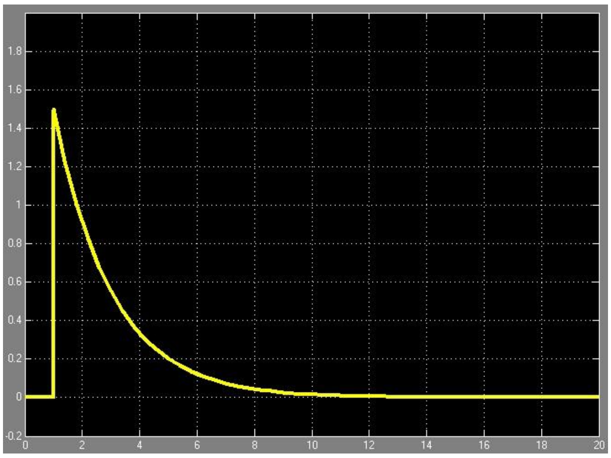
Table 2-24

Parameter	Data type	Description
error	Bool	If the derivative action time <= 0, “error” = TRUE will be set.
status	Word	If the derivative action time <= 0, “status” = 16#8001.
maxReached	Bool	When “maxReached” = TRUE, the “output” output variable was limited to the maximum value “maxOut”.
minReached	Bool	When “minReached” = TRUE, the “output” output variable was limited to the minimum value “minOut”.
output	Real	Output variable (valid only when “error” = FALSE)

Step response

$$y(t) = \frac{tmLag2}{tmLag1} e^{-\frac{t}{tmLag1}}, \qquad t \geq 0$$

Figure 2-24: DT1 element step response



2.2.12 FB „LSim_AllPass1OrdReal" (FB61)

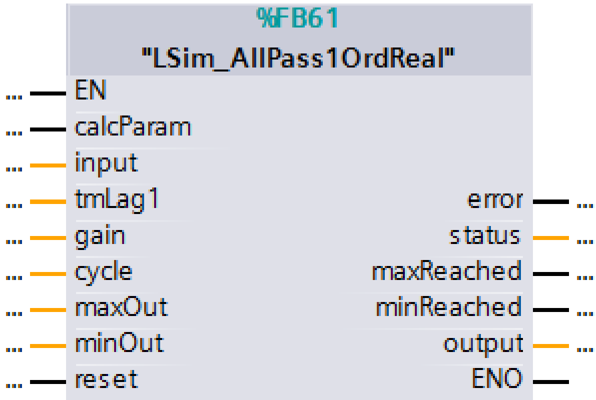
FB "LSim_AllPass1OrdReal" (FB61) simulates a first-order all-pass with real zeros.

Transfer function

$$F(p) = \frac{gain * (1 - tmLag1 * p)}{(1 + tmLag1 * p)}$$

Figure

Figure 2-25



Input parameters

Table 2-25

Parameter	Data type	Description
calcParam	Bool	Recalculation of the internal parameters (activation required when "tmLag1", "gain" or "cycle" changes)
input	Real	Input variable
tmLag1	Real	Time constant
gain	Real	Gain factor
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
maxOut	Real	Maximum limit of the output signal (when the signal is above the maximum limit, "output" = "maxOut" will be set)
minOut	Real	Minimum limit of the output signal (when the signal is below the minimum limit, "output" = "minOut" will be set)
reset	Bool	Resets all relevant parameters to '0', including "output".

Output parameters

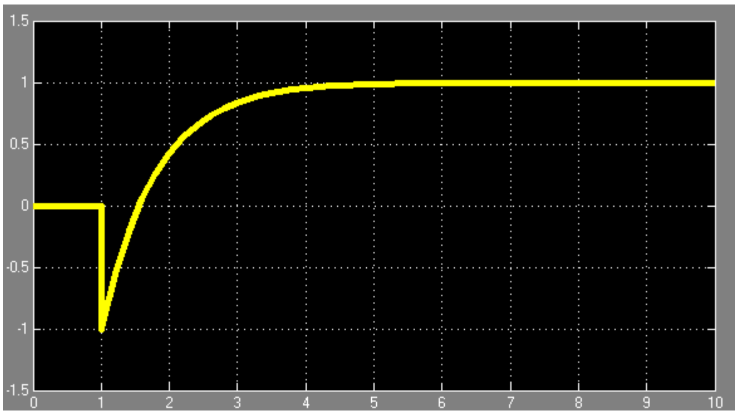
Table 2-26

Parameter	Data type	Description
error	Bool	If the time constant <= 0, "error" = TRUE will be set.
status	Word	If the time constant <= 0, "status" = 16#8001.
maxReached	Bool	When "maxReached" = TRUE, the "output" output variable was limited to the maximum value "maxOut".
minReached	Bool	When "minReached" = TRUE, the "output" output variable was limited to the minimum value "minOut".
output	Real	Output variable (valid only when "error" = FALSE)

Step response

$$y(t) = gain * (1 - 2 * e^{\frac{t}{tmLag1}})$$

Figure 2-26



2.2.13 FB „LSim_AllPass2OrdReal” (FB62)

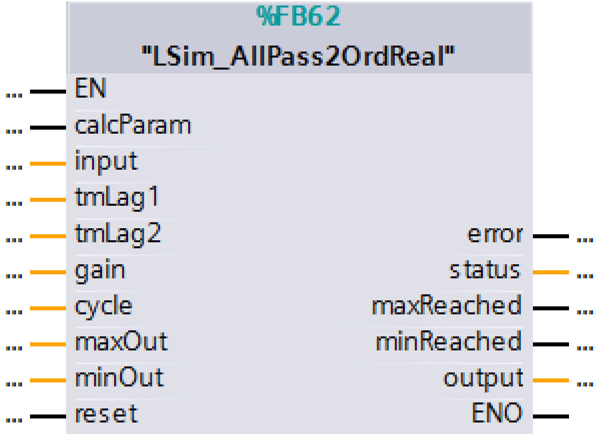
FB “LSim_AllPass2OrdReal” (FB62) simulates a second-order all-pass with real zeros.

Transfer function

$$F(p) = \frac{gain * (1 - tmLag1 * p) * (1 - tmLag2 * p)}{(1 + tmLag1 * p) * (1 + tmLag2 * p)}$$

Figure

Figure 2-27



Input parameters

Table 2-27

Parameter	Data type	Description
calcParam	Bool	Recalculation of the internal parameters (activation required when “tmLag1”, “tmLag2”, “gain” or “cycle” changes)
input	Real	Input variable
tmLag1	Real	Time constant for the first PT1 element (in seconds)
tmLag2	Real	Time constant for the second PT1 element (in seconds)
gain	Real	Gain factor
disturb	Real	Disturbance variable (applied at the “input” input)
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
maxOut	Real	Maximum limit of the output signal (when the signal is above the maximum limit, “output” = “maxOut” will be set)
minOut	Real	Minimum limit of the output signal (when the signal is below the minimum limit, “output” = “minOut” will be set)
reset	Bool	Resets all relevant parameters to ‘0’, including “output”.

Output parameters

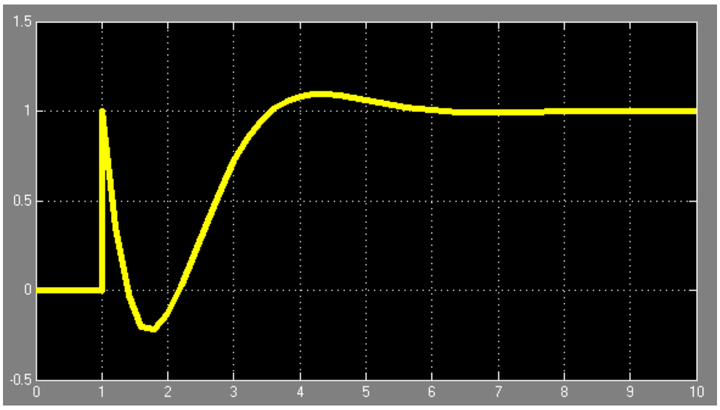
Table 2-28

Parameter	Data type	Description
error	Bool	If the time constants are identical or <= 0, “error” = TRUE will be set.
status	Word	If the time constants are identical or <= 0, “status” = 16#8001 will be output.
maxReached	Bool	When “maxReached” = TRUE, the “output” output variable was limited to the maximum value “maxOut”.
minReached	Bool	When “minReached” = TRUE, the “output” output variable was limited to the minimum value “minOut”.
output	Real	Output variable (valid only when “error” = FALSE)

Step response

$$y(t) = gain * (1 + K * e^{\frac{t}{tmLag1}} - K * e^{\frac{t}{tmLag1 \quad tmLag2}}),\ \text{with}\ K = \frac{2*(tmLag1+tmLag2)}{tmLag2-tmLag1}$$

Figure 2-28

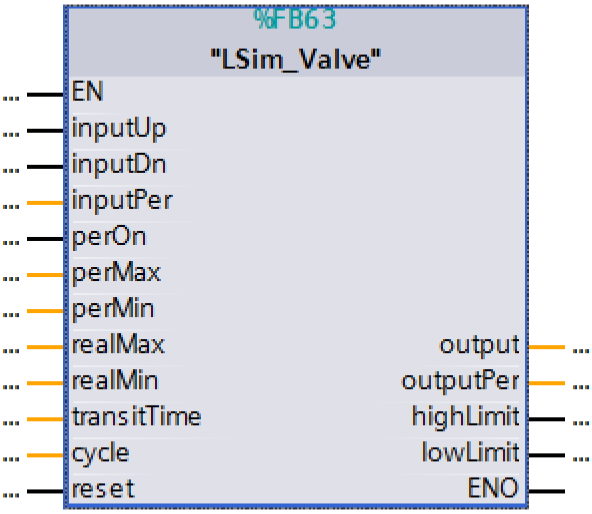


2.2.14 FB „LSim_Valve" (FB63)

FB "LSim_Valve" (FB62) simulates a valve with adjustable travel time.

Figure

Figure 2-29



Input parameters

Table 2-29

Parameter	Data type	Description
inputUp	Real	"Open valve" control signal
inputDn	Real	"Close valve" control signal
inputPer	Int	Analog manipulated variable
perOn	Bool	Selection switch to set whether "inputUp" / "inputDn" or "inputPer" will be used (when TRUE, "inputPer" will be used)
perMax	Real	Upper limit of the analog value of the valve position
perMin	Real	Lower limit of the analog value of the valve position
realMax	Real	Upper limit of the converted floating-point value for the valve position
realMin	Real	Lower limit of the converted floating-point value for the valve position
transitTime	Real	Travel time of the valve between the end positions
cycle	Real	Cycle time of the calling cyclic interrupt OB (in seconds)
reset	Bool	Resets "output" to "realMin"

Output parameters

Table 2-30

Parameter	Data type	Description
output	Real	Calculated valve position (within the "realMin" and "realMax" limits)
outputPer	Int	Calculated analog valve position (within the "perMin" and "perMax" limits)
highLimit	Bool	Simulated upper valve end position reached when "highLimit" = TRUE.
lowLimit	Bool	Simulated lower valve end position reached when "lowLimit" = TRUE.

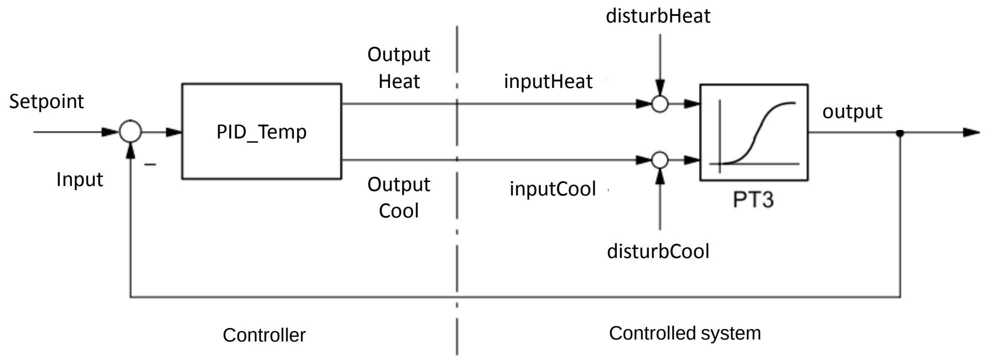
2.2.15 FB „LSim_PT3HeatCool" (FB 64)

FB "LSim_PT3HeatCool" (FB64) simulates a PT3 temperature system.
The block has separate inputs for heating and cooling.

Use case

The block can be used to simulate a temperature system (for example, with the "PID_Temp" technology object).

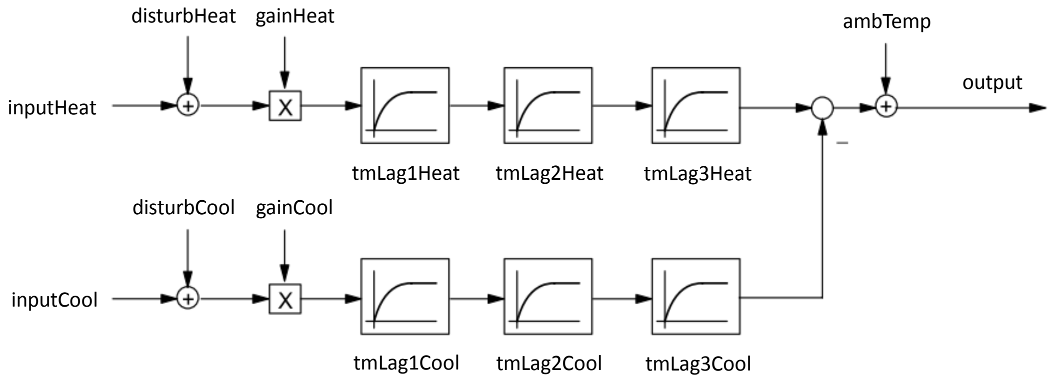
Figure 2-30: Sample control loop "PID_Temp" with "LSim_PT3HeatCool"



Connection diagram

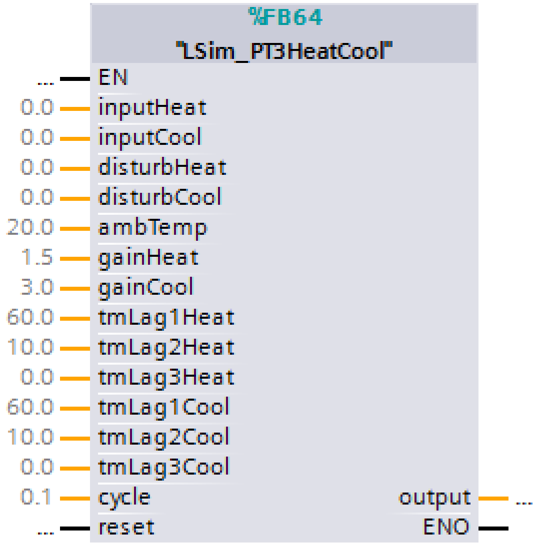
FB "LSim_PT3HeatCool" (FB64) uses the following equivalent connection diagram:

Figure 2-31: Structure and parameters of the "LSim_PT3HeatCool" system block



Figure

Figure 2-32



Parameters

Table 2-31

Parameter	Data type	Description
inputHeat	IN: Real	Input variable (heat)
inputCool	IN: Real	Input variable (cool)
disturbHeat	IN: Real	Disturbance variable (heat); applied at the “inputHeat” input
disturbCool	IN: Real	Disturbance variable (cool); applied at the “inputCool” input
ambTemp	IN: Real	Ambient temperature
gainHeat	IN: Real	Gain factor (heat)
gainCool	IN: Real	Gain factor (cool)
tmLag(x)Heat	IN: Real	x-order lag factor (heat)
tmLag(x)Cool	IN: Real	x-order lag factor (cool)
cycle	IN: Real	Cycle time of the calling cyclic interrupt OB (in seconds)
reset	INOUT: Bool	Reset all relevant parameters (“output” = “ambTemp”); “reset” will be reset autonomously
output	OUT: Real	Output variable (valid only when “error” = FALSE)

3 Working with the Library

What will you find in this section?

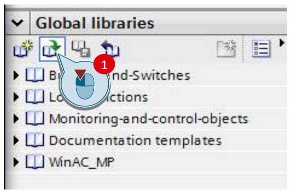
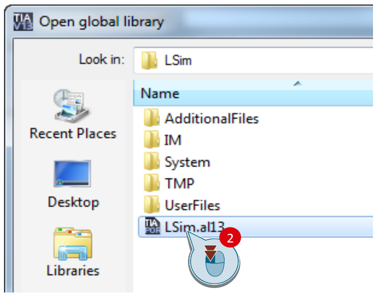
This chapter consists of instructions for integrating the „LSim“ library into your STEP 7 project and instructions for using the library blocks.

3.1 Integrating the library into STEP 7

The table below lists the steps for integrating the „LSim“ library into your STEP 7 project. Subsequently, you can use the blocks of the „LSim“ library.

Note	The following section assumes that a STEP 7 project exists.

Table 3-1

No.	Action	Note
1.	Download the “79047707_LSim_LIB_v20.zip” file and extract the file on your engineering station.	
2.	In the right-hand pane, open the “Libraries” tab and click “Open global library”. Navigate to the storage location of the extracted folder and double-click to open the “LSim” file.	 

3.2 Integrating the library blocks into STEP 7

The table below lists the steps for integrating the blocks of the „LSim“ library into your STEP 7 program.

Table 3-2

No.	Action	Note
1.	Use drag and drop to drag the simulation blocks you want to use from the “Master copies” of the library to the program blocks folder of your user program.	
2.	Now you can use the inserted blocks in your user program to simulate systems.	

3.3 Interconnecting controlling elements

Controlled system

As an example, we will implement a controlled system with the following controlling elements:

Figure 3-1 Controlled system

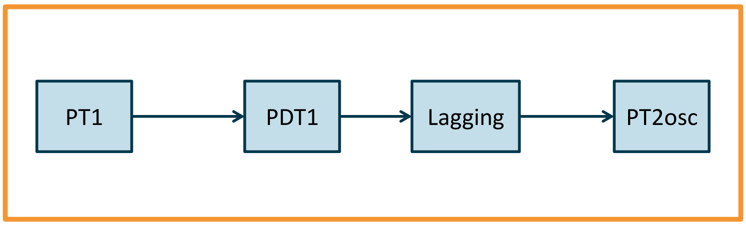
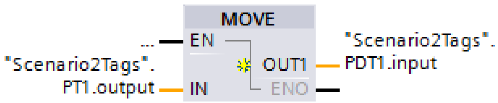


Table 3-3

No.	Action	Note
1.	Add the controlling elements you want to use as described in Table 3-2.In this example, these are the following controlling elements: • PT1 (FB “LSim_PT1”) • PDT1 (FB “LSim_PDT1”) • Lag element (FB “LSim_Lagging”) • PT2 in the periodic case (FB “LSim_PT2osc”)	
2.	Insert the function blocks into your cyclic interrupt OB and between each call, leave one network free for the MOVE commands.	
3.	Use MOVE commands to interconnect the outputs of the controlling elements with the input of the respective following controlling element.	
4.	Now you have set up a complete controlled system. **Note** If you want to monitor the controlled system, you can use the S7-1500 “Traces” function.	

4 Notes and Support

Overview

The “LSim” library is used to simulate controlled systems in STEP 7.

Examples

The documentation “Closed-Loop Control with PID_Compact V2.2” contains examples of how the blocks of the simulation library can be called and used.

The complete documentation, including the library and a sample project, can be downloaded at the following link:

https://support.industry.siemens.com/cs/ww/en/view/79047707

5 Related literature

Table 5-1

	Topic
\1\	Siemens Industry Online Support https://support.industry.siemens.com
\2\	Download page of this entry https://support.industry.siemens.com/cs/ww/en/view/79047707
\3\	Function Manual: SIMATIC S7-1200, S7-1500 PID Control https://support.industry.siemens.com/cs/ww/en/view/108210036
\4\	Application Example: „Single and Multi Loop Controller Structures (Cascade Control) with PID_Temp” https://support.industry.siemens.com/cs/ww/en/view/103526819
\5\	Application Example: “3-Point Stepper Control with SIMATIC S7-1500” https://support.industry.siemens.com/cs/ww/en/view/68011827
\6\	Application Example: PID Control with PID_Compact (S7-1200) https://support.industry.siemens.com/cs/ww/en/view/100746401

6 History

Table 6-1

Version	Date	Modifications
V1.0	08/2013	First version
V1.1	12/2014	“LSim_PT3HeatCool” block added
V2.0	03/2016	Review and publication for STEP 7 V13 SP1